

Advanced VLSI Design (Module 24151)

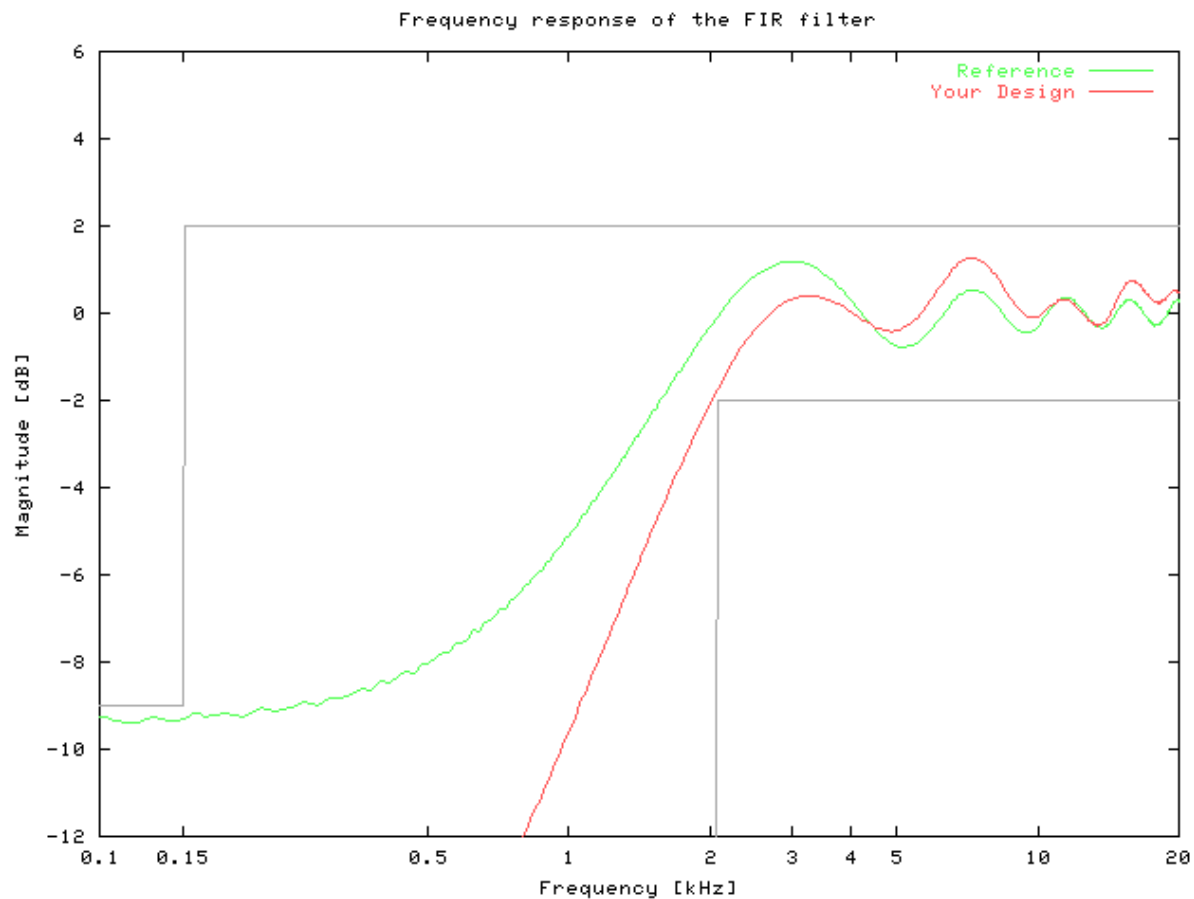
Course and contest

Phase 3 Design improvements

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Frequency response

Achieved attenuation: $-1,98\text{E-}07$

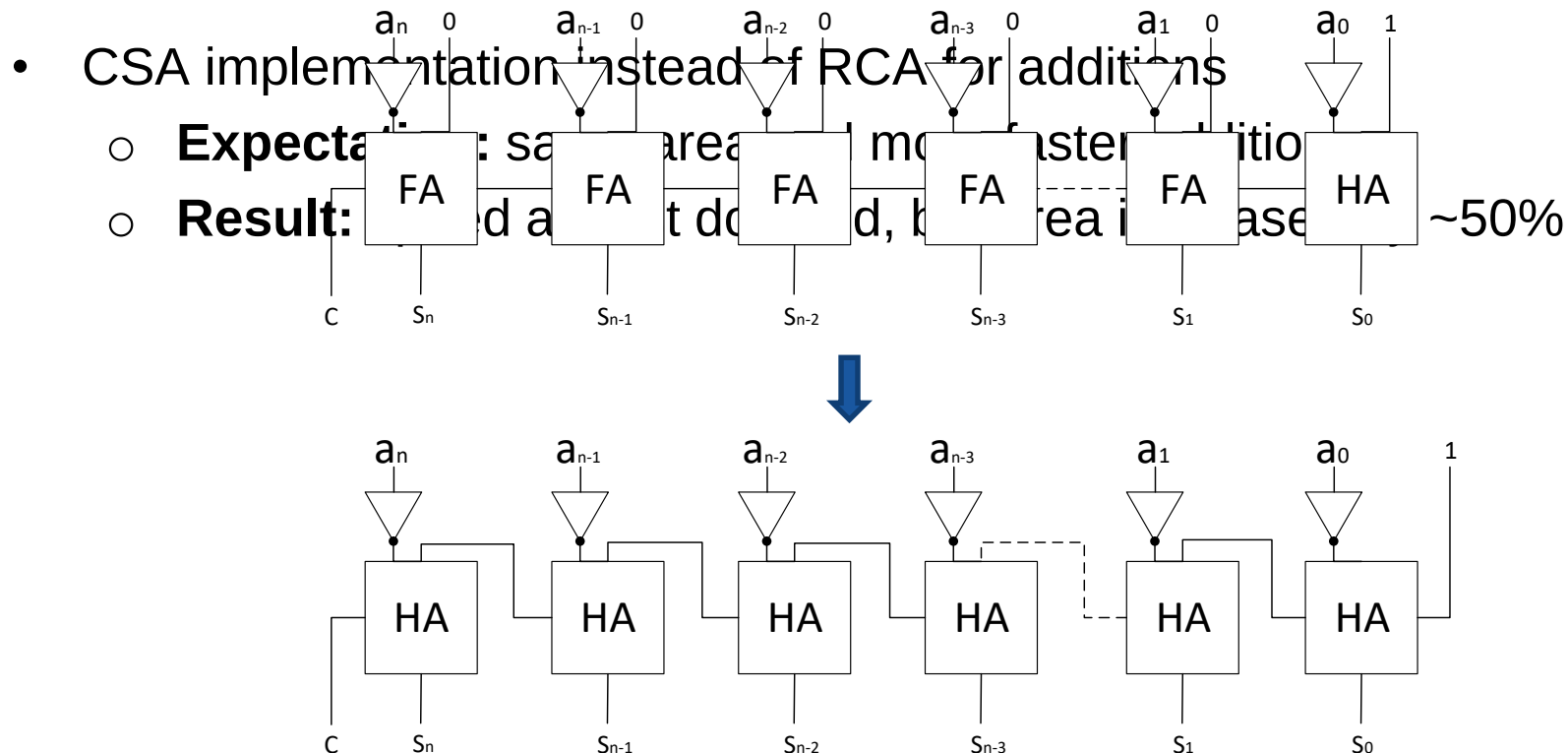


Metrics

Mandatory values for FPGA	
Frequency (MHz)	128,21 MHz
Area A (# of LUT-FF pairs)	972
# LUT	416
# FF	556
Pipeline Stages	3
Power (W)	0,161 W
Attenuation	-1,98E+07
Metric	1,93E+10

Observations

- Always improvement opportunities
 - For example, 2's complement converter



**Thank you for your attention.
Q & A?**